

A report on a seminar on

AI for Beginners: Shaping Engineers for Tomorrow

Date : 28-Jul-2025

Time : 11:00 AM - 01:00 PM

Organized by

Government Polytechnic Jamnagar

Under the initiative of

Student Startup & Innovation Policy (SSIP) Grant

Event Details

Venue : Room No. 207 New Building, G P Jamnagar

Timing : 11:00 AM - 01:00 PM (28-Jul-2025)

Registration Link : NA (ALL EC Students Present)

Organizing Committee Members :

Shri P. H. Kanani (Principal GP Jamnagar)

Shri R. K. Borsaniya (SSIP Co-ordinator)

Shri P. L. Gamit (Event Coordinator)

Event Speaker :

Shri Kishan Patel, Full Stack Developer, Creotex, Rajkot

Participation : 40 Students

28-07-2025

An expert session was conducted on 28th July 2025, from 11:00 AM to 01:00 PM, at Room No. 207, New Building, Government Polytechnic, Jamnagar. The session was specially arranged for Electronics & Communication (EC) Department students, with 40 students in attendance.

The event was organized under the guidance of Shri P. H. Kanani, Principal, Government Polytechnic, Jamnagar, with coordination by Shri R. K. Borsaniya, SSIP Coordinator, and Shri P. L. Gamit, Event Coordinator.

The invited speaker, Shri Kishan Patel, Full Stack Developer, Creotex, Rajkot, explained how full stack technologies can support the development of IoT and other innovative projects under SSIP. He highlighted how web interfaces, backend services and databases can work together to enable data collection, monitoring and control, helping students turn their ideas into functional prototypes.

The session also introduced students to the use of available AI tools for simplifying and accelerating prototype development. Students were guided on how these tools can assist with planning, interface design, code generation and debugging, while emphasizing the need to understand and test the generated output. The discussion encouraged students to combine their electronics knowledge with software development skills to build practical solutions.

The session concluded with concluding remarks and a vote of thanks by Shri P. L. Gamit sir, EC Department faculty. He appreciated the speaker and organizing committee and encouraged students to apply the knowledge gained to their SSIP projects. He emphasized continuous learning, teamwork and the thoughtful use of emerging technologies to transform innovative ideas into working prototypes.

Few Event Photos



Event Brochure



A Seminar on



An introduction to the world of AI FOR BEGINNERS: SHAPING ENGINEERS FOR TOMORROW

EVENT ORGANIZING COMMITTEE

Patron

Shri. P. H. Kanani
Principal
G P Jamnagar

Convener

Shri. R. K. Borsaniya
SSIP Coordinator
G P Jamnagar

Coordinator

Shri. P. L. Gamit
Lecturer, EC Department
G P Jamnagar

EVENT SPEAKER

Shri. Kishan Patel
Full Stack Developer
Creotex LLP, Rajkot

DATE AND PLACE

THURSDAY, 28 JULY 2025
FROM 11:00 AM TO 01:00 PM
AT ROOM 207, NEW BUILDING, G P JAMNAGAR

CONTACT FOR REGISTRATION

✉ gpjssip@gmail.com

🌐 www.gpjamnagar.ac.in

~End of Report~